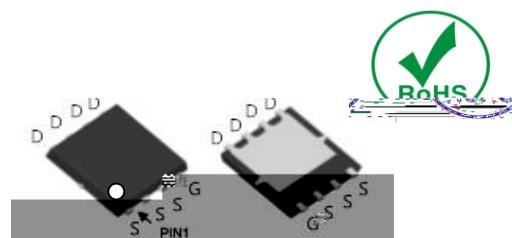


**General Description**

The ZM100N08HN combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

Features

- Trench technology
- $R_{DS(ON)}$ to minimize conductive loss

Product Summary $V_{DS} = 80V$ $R_{DS(ON)} = 9m\Omega$ $I_D = 60A$ 

DFN5 6

Application

Synchronous Rectification for AC-DC/DC-DC converter

Oring switches

Ordering Information:

Part NO.	ZM100N08HN
Marking	ZM100N08H
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

Absolute Maximum Ratings $T_C = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_D@T_C=25^\circ C$	60	A
	$I_D@T_C=75^\circ C$	45.6	A
	$I_D@T_C=100^\circ C$	37.8	A
Pulsed Drain Current	I_{DM}	210	A
Total Power Dissipation($T_C=25^\circ C$)	$P_D@T_C=25^\circ C$	85	W
Total Power Dissipation($T_A=25^\circ C$)	$P_D@T_A=25^\circ C$	3.4	W
Operating Junction Temperature	T_J	-55 to 150	
Storage Temperature	T_{STG}	-55 to 150	
Single Pulse Avalanche Energy@ $L=0.1mH$	E_{AS}	180	mJ
Avalanche Current@ $L=0.1mH$	I_{AS}	60	A



Fig.1 Gate-Charge Characteristics

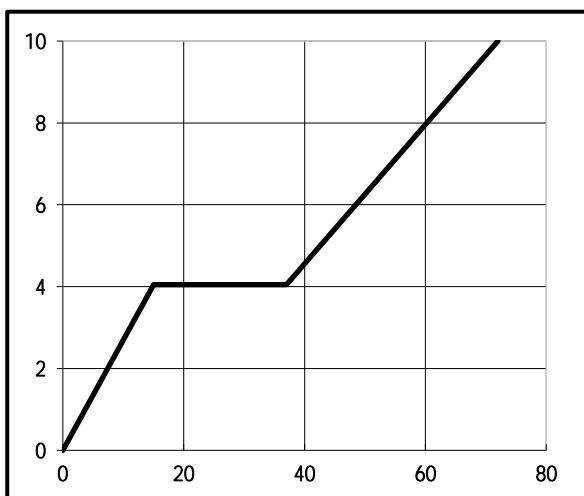


Fig.2 Capacitance Characteristics

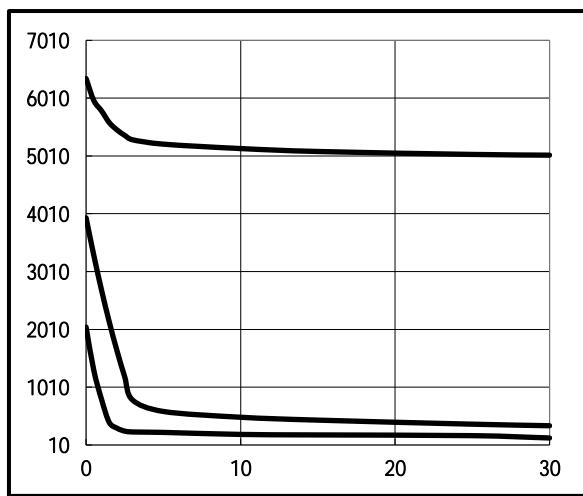


Fig.3 Power Dissipation

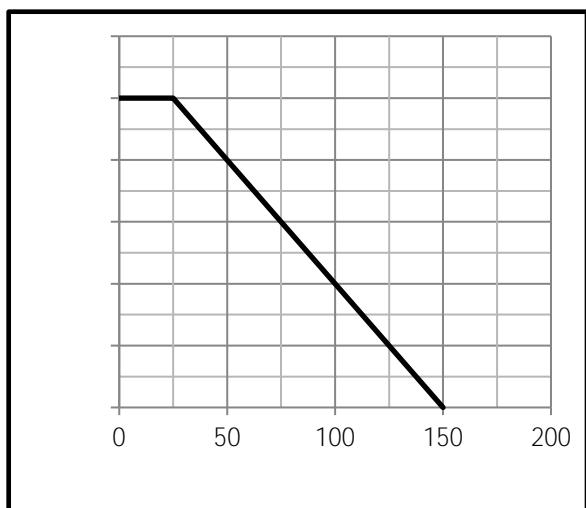


Fig.4 Typical output Characteristics

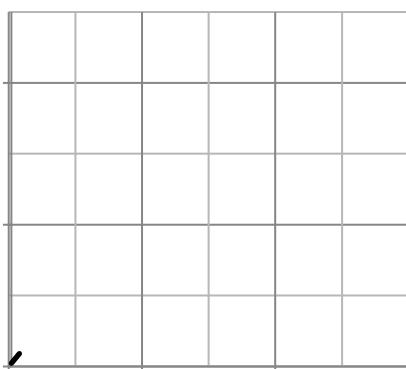


Fig.5 Threshold Voltage V.S Junction Temperature Fig.6 Resistance V.S Drain Current



Fig.7 On-Resistance VS Gate Source Voltage

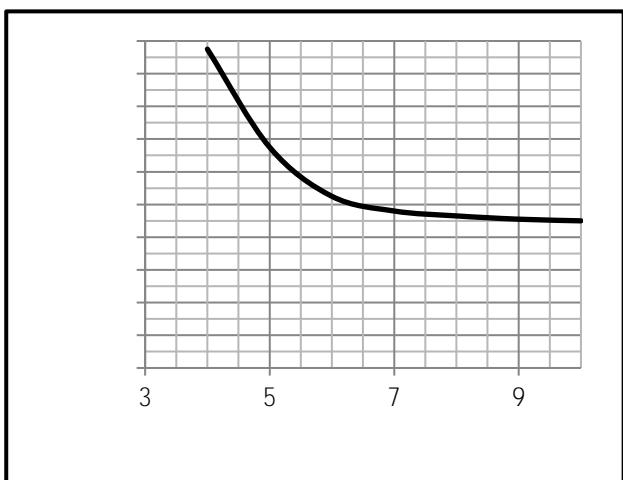


Fig.8 On-Resistance V.S Junction Temperature

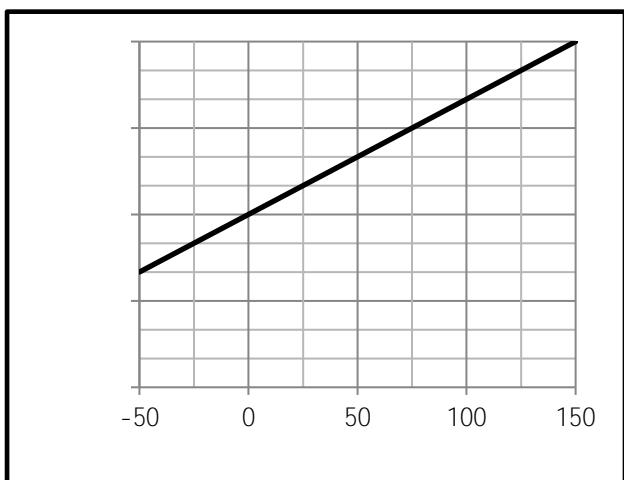


Fig.9 Switching Time Measurement Circuit

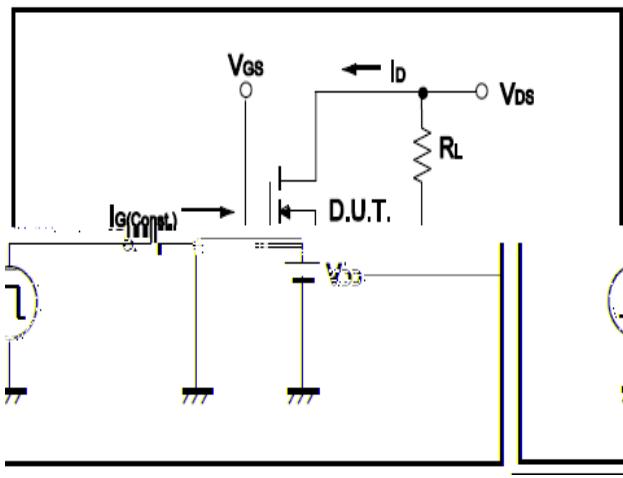


Fig.10 Gate Charge Waveform

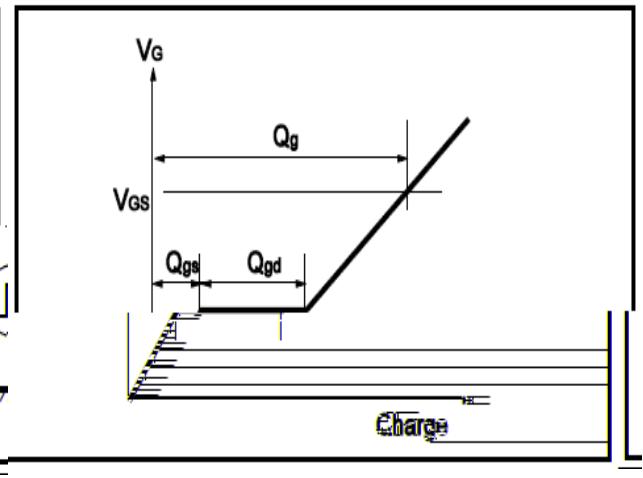


Fig.11 Switching Time Measurement Circuit

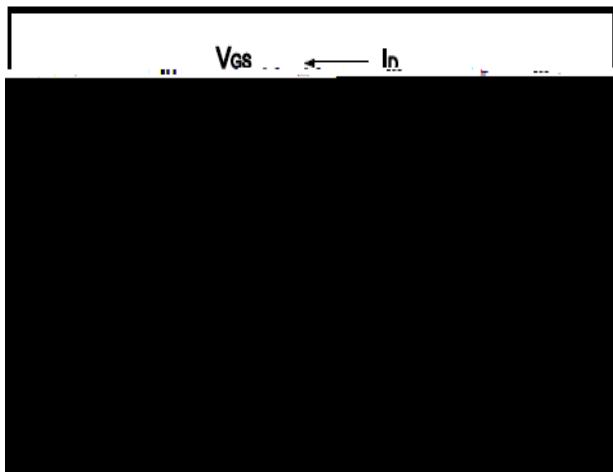
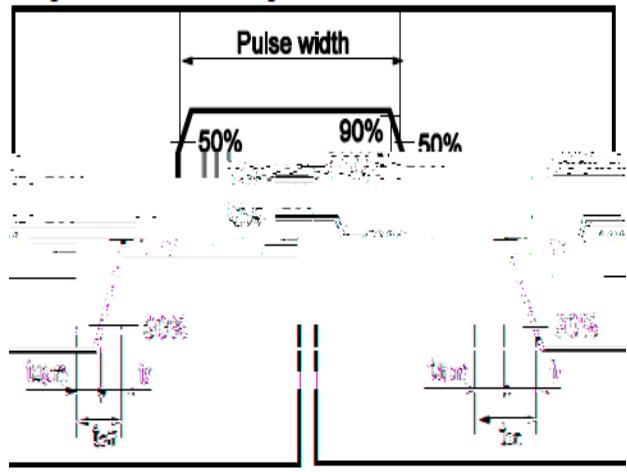


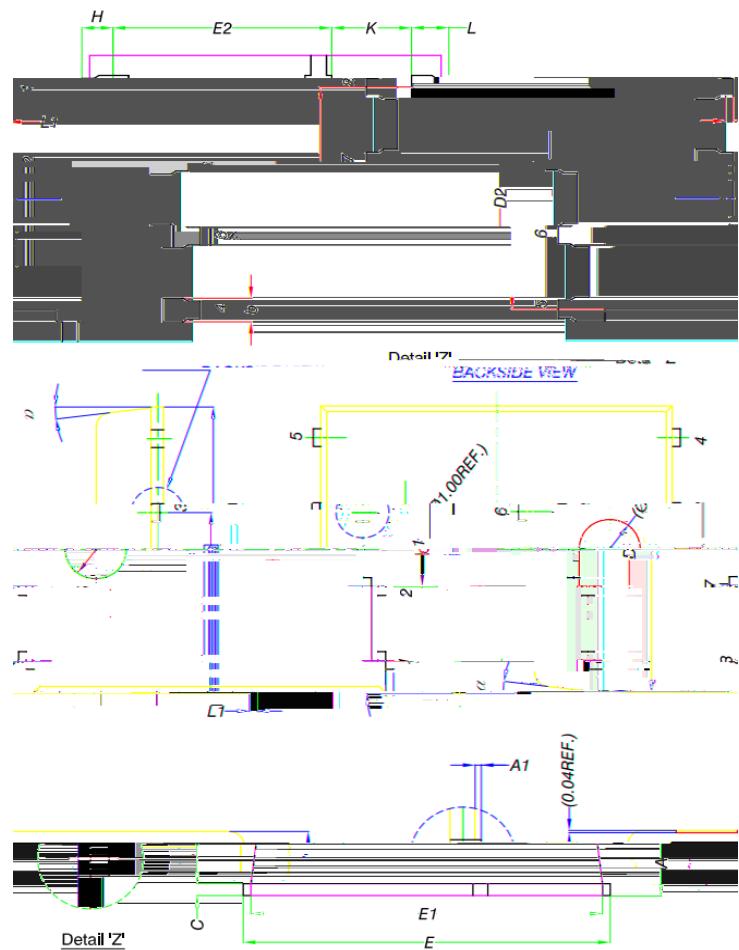
Fig.12 Gate Charge Waveform





Dimensions DFN5x6

Unit mm



MILLIMETERS			
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E1	5.20	5.25	5.30
e	1.78	1.85	1.92
e ₁ 1.27 RSC			
0.413	0.51	0.61	H
1.10	-	-	K
0.51	0.61	0.6	L
0.15	0.06	0.1	0.1